

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|--------|--|---|------------------|---------|------------------|
| L1 | 707715 | motherboard (mother adj3 board) ((printed wiring circuit) adj4 board) pcb pb | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/03/31 08:49 |
| L2 | 10756 | (bare naked) with (die chip ic dice (integrated adj circuit) semiconductor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/03/31 08:50 |
| L3 | 1972 | module and 1 and 2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/03/31 08:50 |
| L4 | 962 | (pin pins) and 3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/03/31 08:51 |